

## **Embedded Systems Course**



## Practical Assignment Solution Sheet (PASS) for Laboratory Practical 4

STUDENT #1:	
STUDENT #2:	
TIME START:	
TIME HANDIN:	
DOC REF:	PRAC04-PASS

## Preamble

Dear Student,

Please answer the "panic" questions in the Prac04 tutorial using this answer sheet.

Please **fill out your names** and **start time** above. Then, before you hand in, fill out the **handin time**. The start and handin times are used only to gather statistics and does not affect your mark.

## **YOUR ANSWERS**

**CHECKLISTS:** Cross off these points to ensure you've done everything

[ ] 2.4: Q-1, Q-2, Q-3 [ ] 2.5: TODO [3.1] – [3.3], Q-4, Q-5, Q-6 [ ] 2.5: TODO [4], Q-7

PLEASE ALSO COMPLETE THE SHORT SURVEY ON BOTTOM OF LAST PAGE

		n be linked to a	one PIO pad?			
	many GPIO pins can		-			
	many GPIO pins can tele one of a,b,c or d be		•			
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(Circ	ele one of a,b,c or d b	(c) 2  abedded peripherible to the AR	(d) 3 neral I/O line lin M920T core and			
(Circ (a) 1 Who pad a leg o	None (b) 1  chooses when an emand thus made access	(c) 2  abedded periphetible to the AR (Circle one of	(d) 3 neral I/O line lin M920T core and			
(Circ (a) I Who pad a leg of	None (b) 1  chooses when an emand thus made access of the chip package)?	(c) 2  abbedded peripherible to the AR (Circle one of the decides, or	(d) 3 neral I/O line lin M920T core and			
(Circ (a) I Who pad a leg of (a) I (b) I	None (b) 1  chooses when an emand thus made access of the chip package)?	(c) 2  abedded peripherible to the AR (Circle one of the decides, or the decides)	(d) 3 neral I/O line lin M920T core and a or b)			
(Circ (a) I Who pad a leg of (a) I (b) I	None (b) 1  chooses when an emand thus made access of the chip package)?  The hardware develope	(c) 2  abedded peripherible to the AR (Circle one of the decides, or the decides)  the decides  the correct and the endent data the	(d) 3 neral I/O line line M920T core and a or b) newer)	d external device	es (if the pad is con	nnecto

		e prac? Any io	deas how it c	ould be made	e easier with
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		SURVEY QUESTIONS					
Number	of diag	these apply to you: [ ] Prac too hard [ ] Prac too easy [ ] Prac too long rams was: [ ] Sufficient [ ] Too few					
)ıagram	ns Comp	plexity was: [ ] Satisfactory [ ] Too complicated					
ndicate	which i	illustrations you understood (by ticking corresponding cell in A column), and which you did					
		(by ticking corresponding B column)					
	В	Illustration Caption					
A		Illustration 1: Overview of PIO Controller (pg 334 of AT92RM9200 datasheet).					
<u>A</u>		Illustration 2: Output control logic for the PIO controller.					
<u>A</u>		musuation 2. Output control togic for the FTO controller.					
<u>A</u>							
<i>A</i>		Illustration 3: Input control logic for the PIO controller.					
<i>A</i>		Illustration 3: Input control logic for the PIO controller.  Illustration 4: Example for demonstration the use of PIV values.					
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	NAL CO	Illustration 3: Input control logic for the PIO controller.  Illustration 4: Example for demonstration the use of PIV values.					

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